

Application No. 10/043,763
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS
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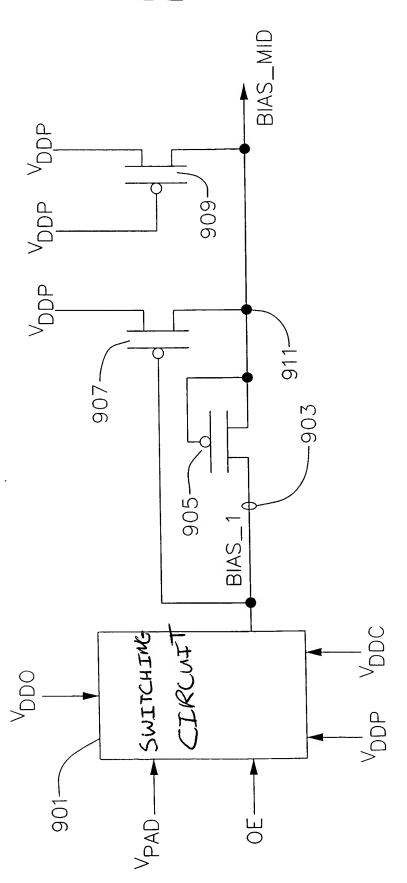


FIG.9A

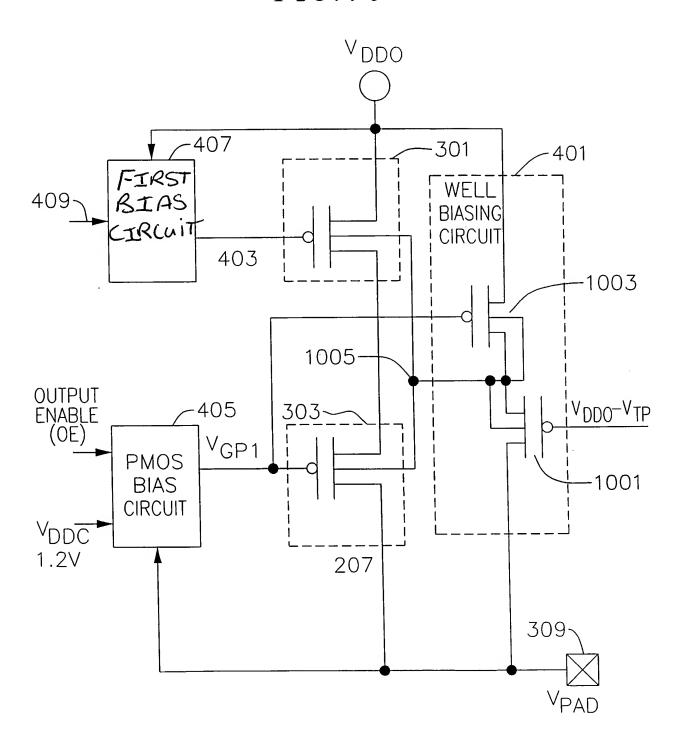


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## FIG. 10

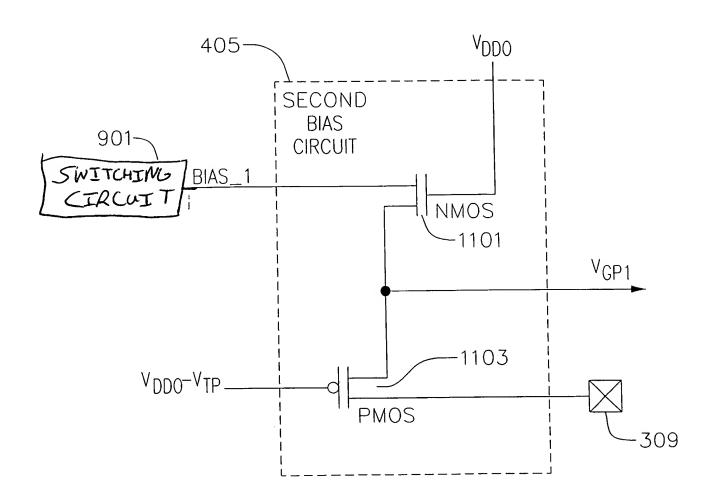




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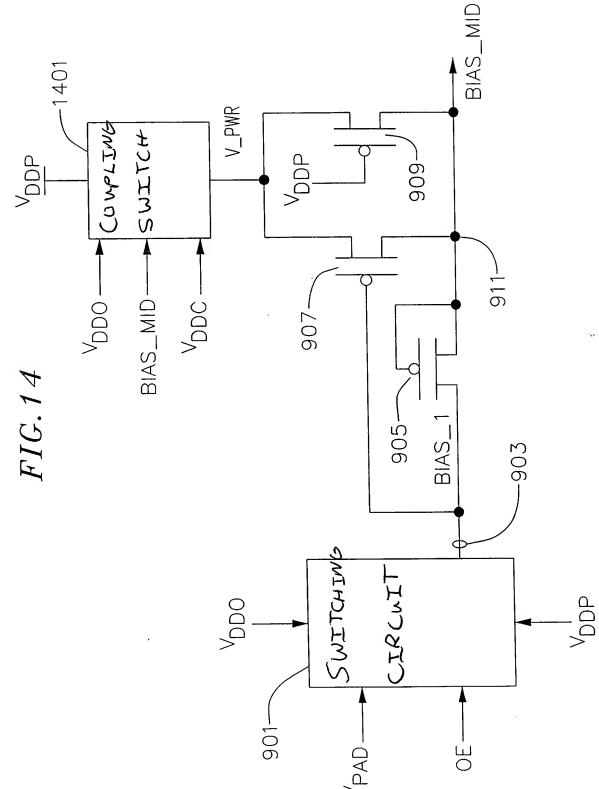
## FIG. 11A





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## FIG. 18

